

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SYSTEM AND METHOD FOR PROTECTING AGAINST SHORT CIRCUITS IN ELECTRIC POWER DISTRIBUTION ARCHITECTURES WITH TWO VOLTAGE LEVELS
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Application Number :

Date :

First Named Applicant: Carles Borrego Bel

Confirmation Number:

Attorney Docket Number: 8153ES

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Documents being submitted:	Files
us-fee-sheet	8153ES-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	8153ES-usidst.xml us-ids.dtd us-ids.xsl
us-request	8153ES-usrequ.xml us-request.dtd us-request.xsl
application-body	8153ES-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd 8153ES_Fig_1.tif 8153ES_Fig_2.tif
us-declaration	8153ES_Declaration_1.tif
us-declaration	8153ES_Declaration_2.tif
Comments	